



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Shin FUJITA

Application No.: 10/629,590

Docket No.: 116725

Filed: July 30, 2003

For: TIMING ADJUSTMENT CIRCUIT, DRIVE CIRCUIT, ELECTROOPTIC DEVICE  
AND ELECTRONIC EQUIPMENT

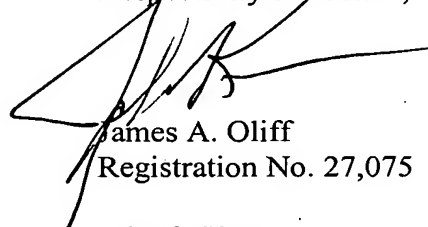
**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,



James A. Oliff  
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JAO:JSK/kap

Date: November 7, 2003

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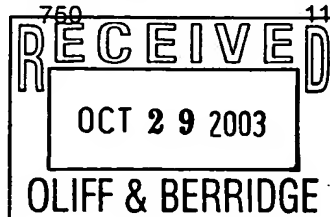


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/629,590	07/30/2003	2184	750	116725	8	16	3

25944  
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CONFIRMATION NO. 6224

## FILING RECEIPT



\*OC000000011122909\*

Date Mailed: 10/28/2003

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## Applicant(s)

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Chino-shi

## Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2002-233881 08/09/2002

If Required, Foreign Filing License Granted: 10/28/2003

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Timing adjustment circuit, drive circuit, electrooptic device and electronic equipment

Preliminary Class

714

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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